

Class	Subc	ISSUE CLASSIFICATION
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U.S. UTILITY Patent Application

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APPLICATION NO. 09/967094	CONT/PRIOR	CLASS 257	SUBCLASS 774	ART UNIT 2811 2826	EXAMINER F. [unclear]
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APPLICANTS

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TITLE

Barrier layer for interconnect structures of a semiconductor wafer
and method for depositing the barrier layer

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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	_____ (Primary Examiner) (Date)		ISSUE FEE	
			Amount Due	Date Paid
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